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In the United States Patent and Trademark Office**Date:** March 28, 2006**In re Application of:** Gaynes et al.**Filed:** 11/28/2003**For:** Optimized Conductive Lid Mounting For Integrated Circuit Chip
Carriers**Serial Number:** 10/707,206**Art Unit:** 2811**Examiner:** Junghwa M. Im**AMENDMENTS AND RESPONSE UNDER 37 C.F.R. § 1.111**Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action mailed on December 28, 2005, which is due for response by March 28, 2006. Any fees required in entering this response may be charged to Applicant's deposit account, 09-0456.

It is respectfully requested that reconsideration of the application in view of the following remarks and amendments. No new matter has been included.

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SN 10/707,206

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